

FIG. 1

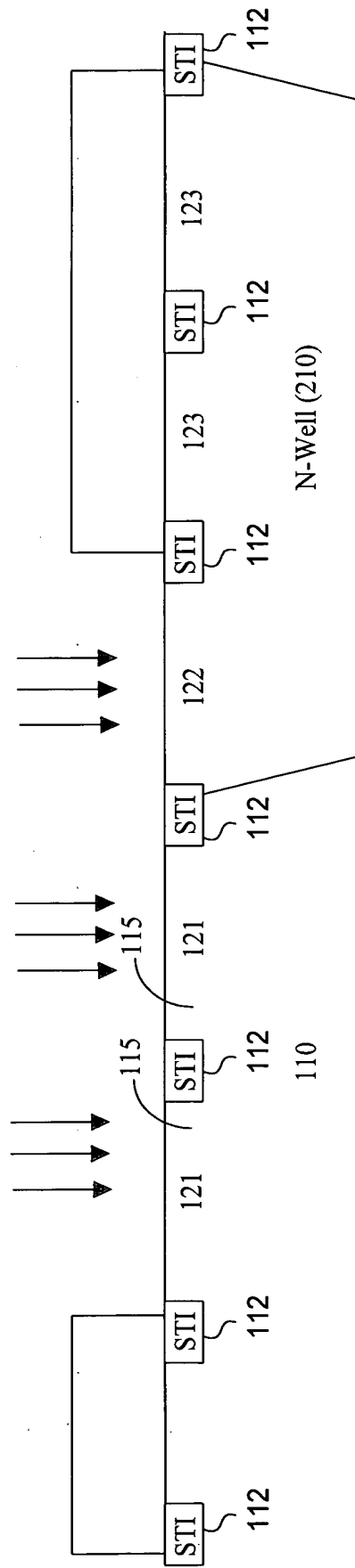


FIG. 2

N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
--------------------------------	-------------------------------	-------------------------------	--------------------------------	-------------------------------	-------------------------------

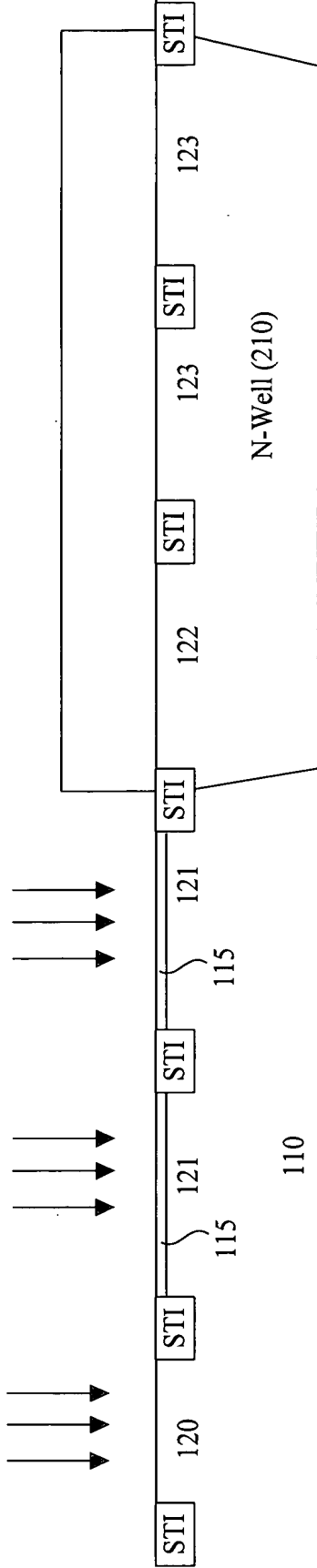


FIG. 3

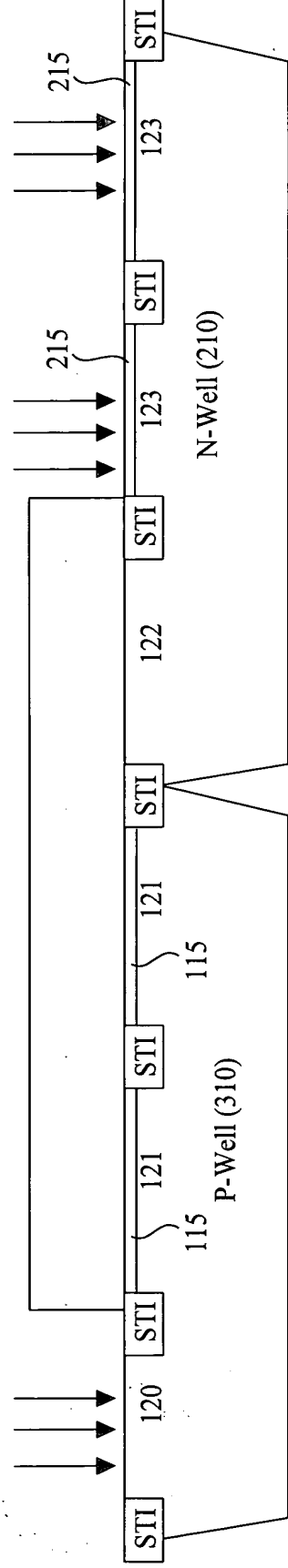
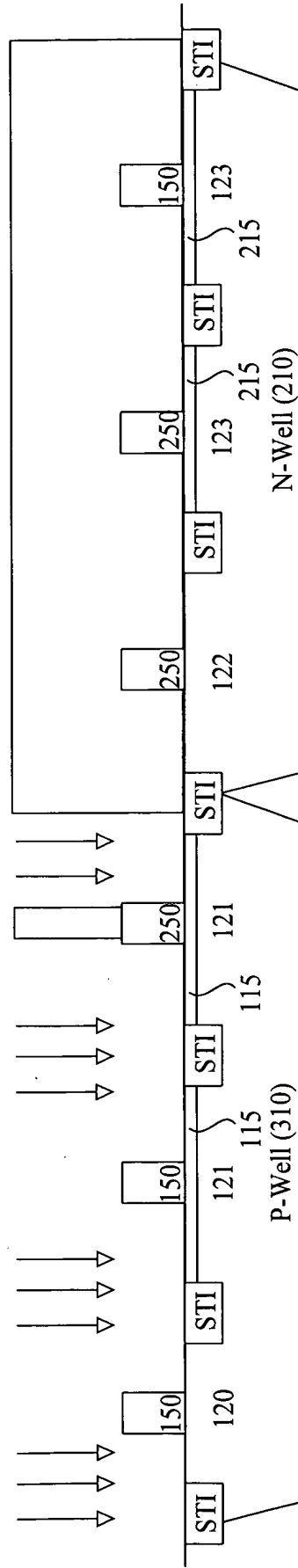


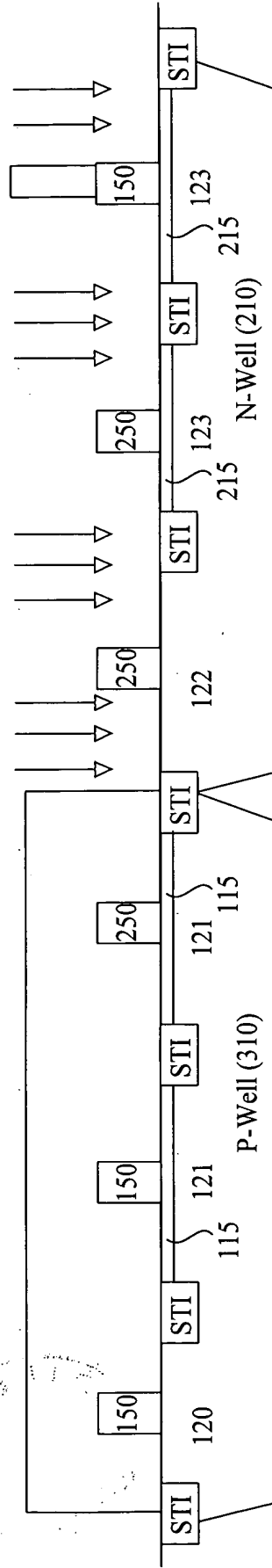
FIG. 4

N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
--------------------------------	-------------------------------	-------------------------------	--------------------------------	-------------------------------	-------------------------------



110

FIG. 7



110

FIG. 8

N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
--------------------------------	-------------------------------	-------------------------------	--------------------------------	-------------------------------	-------------------------------

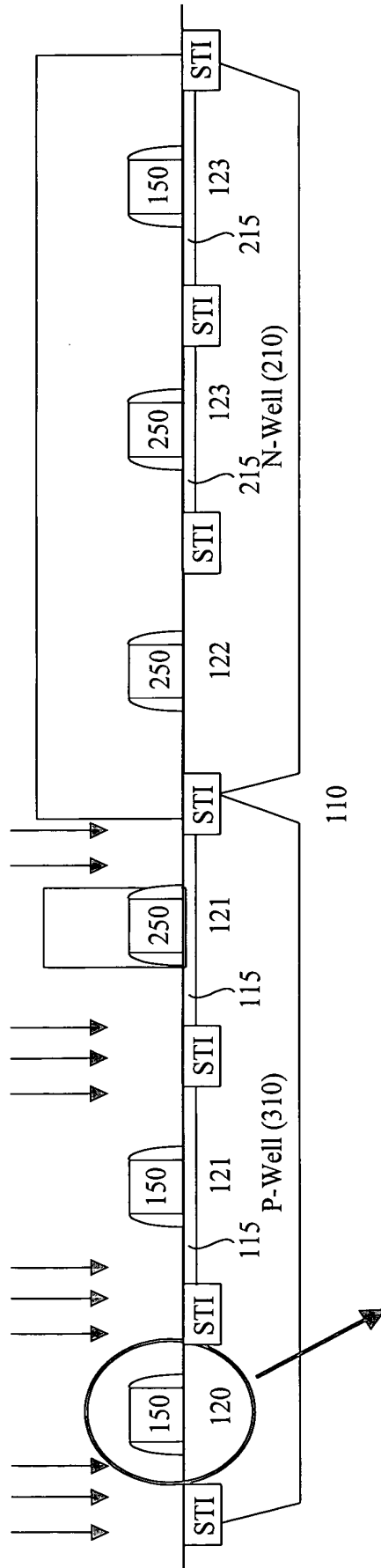


FIG. 9A

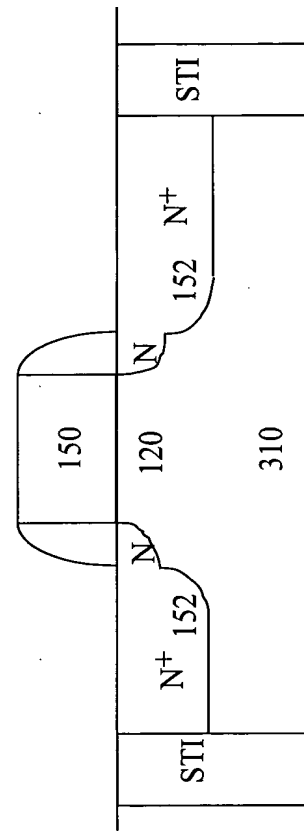


FIG. 9B

N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
--------------------------------	-------------------------------	-------------------------------	--------------------------------	-------------------------------	-------------------------------

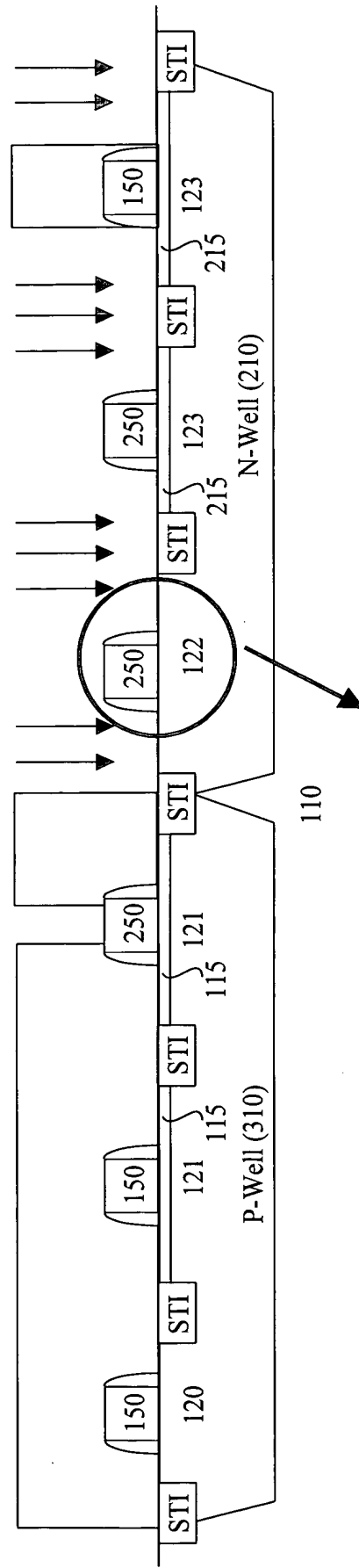


FIG. 10A

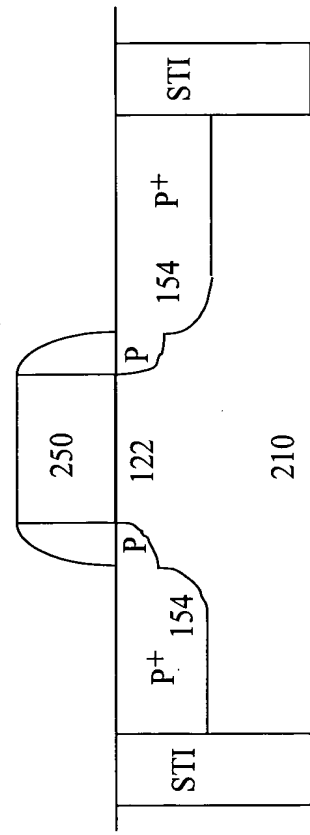


FIG. 10B

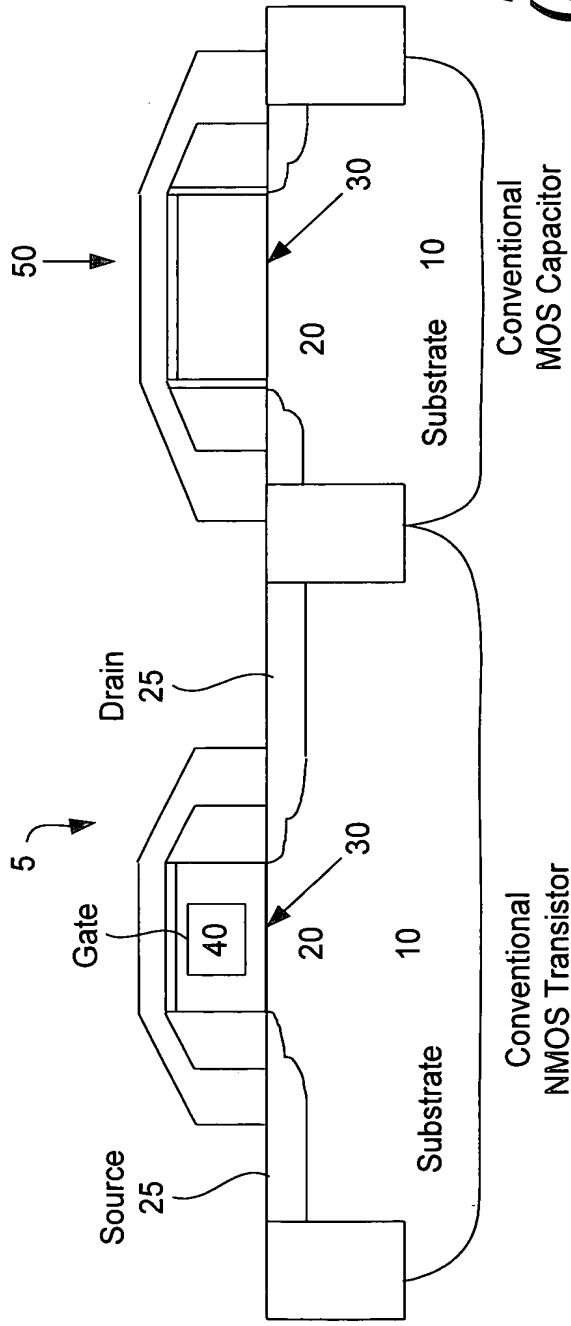


FIG. 11A
(Prior Art)

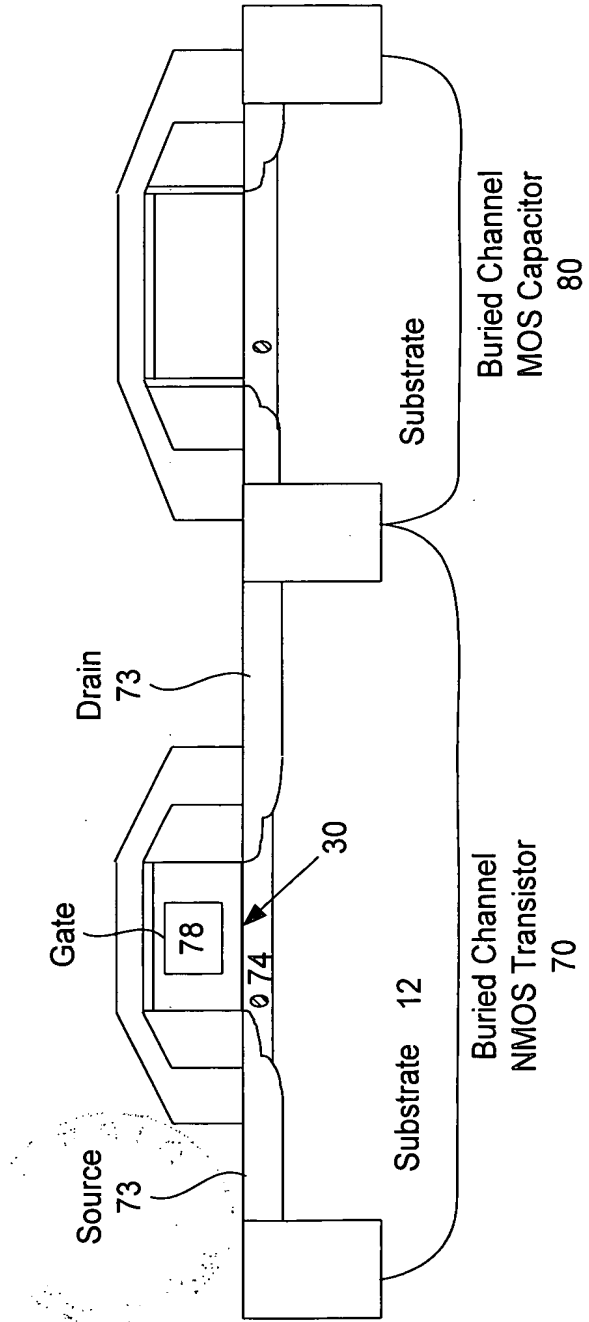


FIG. 11B

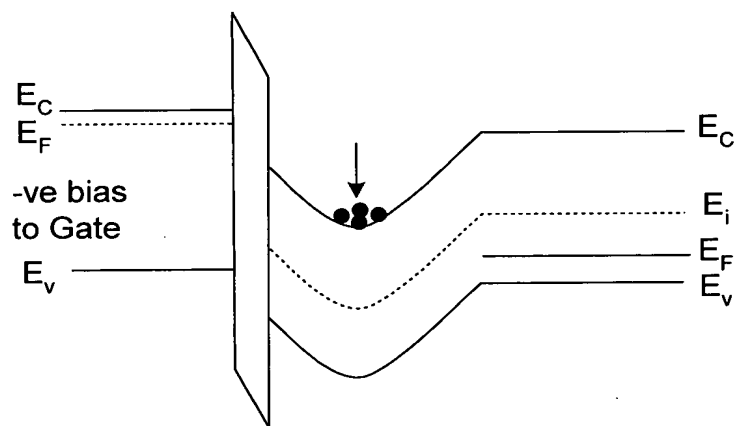


FIG. 12C

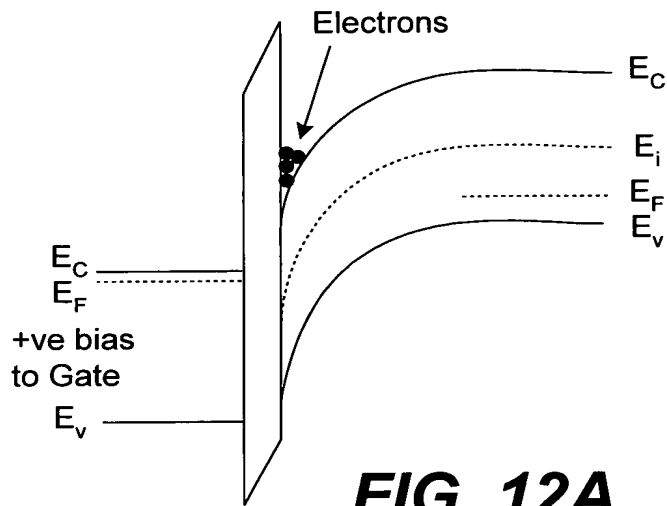


FIG. 12A
(Prior Art)

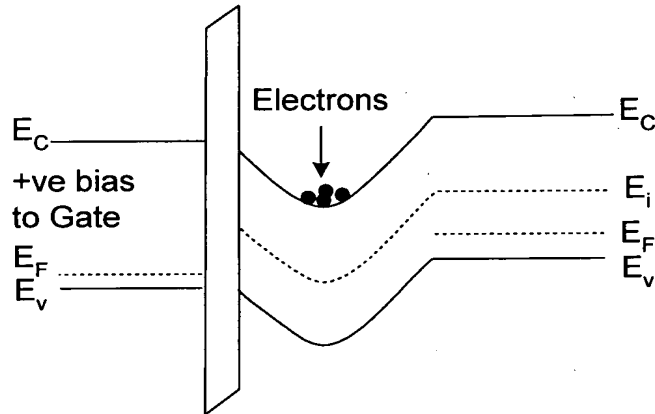


FIG. 12B